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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

ALBERT ET AL.

Serial No. : 09/668,320

Patent No.: 6,968,306 B1

Issued: Nov. 22, 2005

For: Method and System for  
Determining an Interconnect Delay  
Utilizing an...(ECM) Signal Delay  
Model

ATTY. DOCKET NO. AUS920000349US1

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Examiner: H. DAY

**Certificate**  
JUL 07 2006  
**of Correction**

**REQUEST FOR CERTIFICATE OF CORRECTION**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Sir:

Assignee respectfully submits the enclosed proposed Certificate of Correction (in duplicate), which corrects a clerical error made by the U.S. Patent and Trademark Office in Letters Patent No. US 6,968,306 B1, issued Nov. 22, 2005.

Respectfully submitted,

*Brian F. Russell*

Brian F. Russell  
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ATTORNEY FOR ASSIGNEE

**CERTIFICATE OF MAILING**  
37 C.F.R. § 1.8(a)

I hereby certify that this correspondence is being deposited with the United States Postal Service as First-Class Mail in an envelope addressed to Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on the date below.

6/29/06  
Date

*Wendy Delgrosso*  
Signature

JUL 20 2006

(Also Form PTO-1050)

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION****PATENT NO : US 6,968,306 B2****DATED : Nov. 22, 2005****INVENTOR(S) : Alpert et al.**

It is certified that errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

**In Col. 6, lines 51-52, please insert -- . . . -- at the end of the equation.--**

**MAILING ADDRESS OF SENDER:**

**Dillon & Yudell LLP  
Suite 2110  
8911 N. Capital of Texas Hwy.  
Austin, Texas 78759**

**PATENT NO. US 6,968,306 B1**

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(Also Form PTO-1050)

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**PATENT NO : **US 6,968,306 B2**DATED : **Nov. 22, 2005**INVENTOR(S) : **Alpert et al.**

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